| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|----------|------|--|---|---------------------|---------|------------------|
| L1 | 117 | "memory controller" and "data collision" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 16:16 |
| L2 | | L1 and "out of order" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 16:16 |
| L3 | 0 | "out of order" and "data collision" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 16:17 |
| L4 | 17 | "out-of-order" and "data collision" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR , | OFF | 2004/12/03 16:17 |
| L5 | 15 | L4 and "memory controller" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 16:41 |
| L6 | 10 | "memory controller" and "schedule transaction" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 16:44 |
| L7 | | L6 and "DRAM" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 16:47 |
| L8 | 219 | "DRAM" and "data collision" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 16:48 |

| L9 | 15 | L8 and "out-of-order" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 16:48 |
|-----|----|----------------------------|---|----|-----|------------------|
| L10 | 15 | L9 and "memory controller" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 16:48 |

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|----------|-------|---|---|---------------------|---------|------------------|
| L1 | 1 | "memory rank" and "time domain" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 11:03 |
| L2 | 13528 | "memory controller" and "timing" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 11:03 |
| L3 | 370 | L2 and "memory components" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 11:04 |
| L4 | 37 | L3 and "Serial Presence Detect" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 11:35 |
| L5 | 0 | L4 and "rank" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 11:05 |
| L6 | 4433 | (711/100-200).ccls. and "memory controller" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 11:36 |
| L7 | 1073 | L6 and "initialization" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 11:37 |
| L8 | 43 | L7 and "read latency" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 11:37 |

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| L9 | 30 | L8 and "delay" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 11:43 |
| L10 | 303 | "memory controller" and "read latency" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 11:44 |
| L11 | 247 | L10 and "timing" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 11:45 |
| L12 | 194 | L11 and "delay" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 11:45 |
| L13 | 41 | L12 and "distinct" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 11:55 |
| L14 | 6 | "6134638".pn. or "6154821".pn. or "6801985".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/12/03 11:55 |